

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|---|--|------------------|---------|---------------------|
| L1 | 3075 | ("JOHNSON, RICHARD" or "MORGAN, ANDREW" or "ANVIN, H. PETER" or "TORVALDS, LINUS") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:08 |
| L2 | 2 | 1 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:08 |
| L3 | 509 | "TRANSMETA CORPORATION" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:09 |
| L4 | 3 | 3 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:10 |
| L5 | 0 | 3 and (("memory" adj "architecture") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:10 |
| L6 | 2 | ((("memory" adj "architecture") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:10 |
| L7 | 0 | 3 and (("memory" with "architecture") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:11 |
| L8 | 99 | ((("memory" with "architecture") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:11 |
| L9 | 80 | 8 and (("storage" or "memory") with (encrypt\$3 or "program" or instruction \$1)) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:11 |
| L10 | 0 | 9 and ((encrypt\$3 with ("information" or "data")) with (("plain" adj "text") or "plaintext")) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:12 |
| L11 | 12765 | ((("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:15 |
| L12 | 4 | 11 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:16 |

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| L13 | 2751 | 713/189 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:17 |
| L14 | 52 | 113 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:17 |
| L15 | 15 | 114 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:17 |
| L16 | 0 | 115 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:18 |
| L17 | 3157 | 711/147 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:19 |
| L18 | 268 | 117 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:19 |
| L19 | 7 | 118 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:19 |
| L20 | 0 | 119 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:19 |
| L21 | 5632 | 711/170 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:20 |
| L22 | 326 | 121 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:20 |
| L23 | 26 | 122 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:20 |
| L24 | 0 | 123 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:20 |
| L25 | 372 | 713/173 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:21 |
| L26 | 0 | 125 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:21 |

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| L27 | 12 | 25 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:21 |
| L28 | 0 | 27 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:22 |
| L29 | 442 | 710/13 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:22 |
| L30 | 9 | 29 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:23 |
| L31 | 0 | 30 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:23 |
| L32 | 7 | 29 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:23 |
| L33 | 0 | 32 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:23 |
| L34 | 3758 | 709/213 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:24 |
| L35 | 114 | 34 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:25 |
| L36 | 3 | 35 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:25 |
| L37 | 0 | 36 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:25 |
| L38 | 0 | 35 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:25 |
| L39 | 1343 | 709/214 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:26 |
| L40 | 57 | 39 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:26 |

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| L41 | 1 | [40 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:26 |
| L42 | 0 | [41 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:26 |
| L43 | 0 | [40 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:27 |
| L44 | 1013 | 709/215 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:27 |
| L45 | 85 | [44 and ("memory" adj "architecture")] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:27 |
| L46 | 18 | [45 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:27 |
| L47 | 0 | [46 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:27 |
| L48 | 1764 | 709/216 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:28 |
| L49 | 52 | [48 and ("memory" adj "architecture")] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:28 |
| L50 | 1 | [49 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:28 |
| L51 | 0 | [50 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:29 |
| L52 | 0 | [49 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor")))] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:29 |
| L53 | 308 | 712/14 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:29 |
| L54 | 28 | [53 and ("memory" adj "architecture")] | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:29 |

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| L55 | 0 | 54 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:30 |
| L56 | 0 | 54 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:30 |
| L57 | 183 | 712/27 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:30 |
| L58 | 10 | 57 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:30 |
| L59 | 0 | 58 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:31 |
| L60 | 0 | 58 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:31 |
| L61 | 0 | 57 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:31 |
| L62 | 13919 | 707/100 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:31 |
| L63 | 57 | 62 and ("memory" adj "architecture") | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:32 |
| L64 | 3 | 63 and (("memory" or "storage") with ((protect\$3 or unprotect\$3) with ("area" or "space"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:32 |
| L65 | 0 | 64 and (("memory" adj "space") with ("outside" with ("processor" or "microprocessor"))) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2009/02/01 19:33 |

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